

MUH-12041

CERTIFICATION OF MAILING

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Date:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.: 09/997,983

Confirmation No.:4244

Inventor : Filed :

Heinz Hönigschmid November 29, 2001

Art Unit

2818

Examiner

Trong Q. Phan

Title

Integrated Magnetoresistive Semiconductor
Memory and Fabrication Method for the Memory

Docket No. :

MUH-12041

Cust. No.

24131

AMENDMENT

Mail Stop Non Fee Amendment Hon. Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated October 14, 2003 kindly amend the above-identified application as follows:

Amendments to the Specification begin on page 3 of this paper.

Appl. No. 09/997,983

Amdt. Dated January 14, 2004

Reply to Office Action of October 14, 2003

Amendments to the Claims are reflected in the listing of claims which begins on page 9 of this paper.

Remarks/Arguments begin on page 13 of this paper.